



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Warren M. Farnworth

Serial No.: 09/548,942

Filed: April 13, 2000

For: METHOD AND APPARATUS OF
INTERPOSING VOLTAGE REFERENCE
TRACES BETWEEN SIGNAL TRACES IN
SEMICONDUCTOR DEVICES

Confirmation No.: 6934

Examiner: B. Lee

Group Art Unit: 2817

Attorney Docket No.: 2269-4161US
(98-1265.00/US)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

April 4, 2006
Date

Signature

Erika Gandre

Name (Type/Print)

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following amendments and remarks are filed in response to the Examiner's remarks in the Office Action mailed January 4, 2006, the three-month shortened statutory period for response to which expires on April 4, 2006.

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 4 of this paper.

Amendments to the Drawings begin on page 8 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 9 of this paper.

An **Appendix** including amended drawing figures is attached following page 16 of this paper.